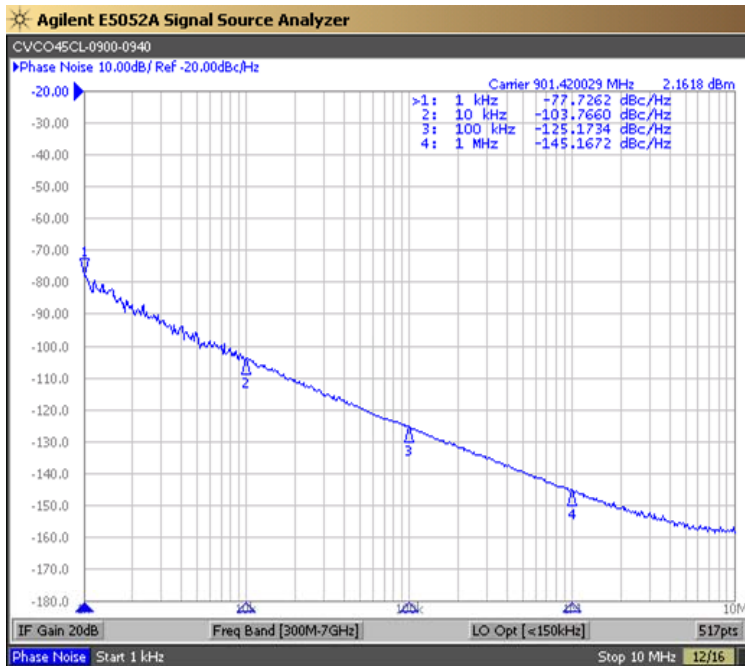


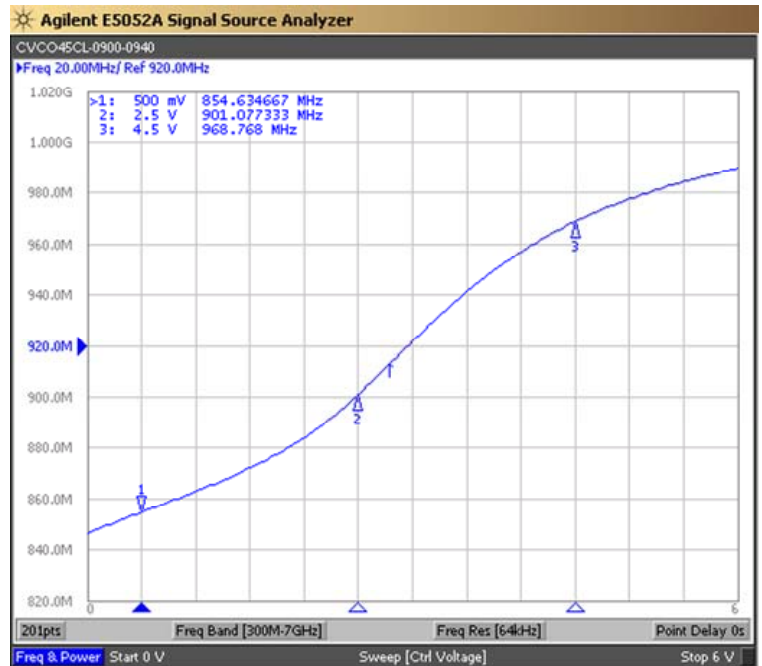


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			900	MHz
Upper Frequency:	940			MHz
Tuning Voltage:	0.5		4.5	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	-3.0	0	+3.0	dBm
Supply Current:		17		mA
Harmonic Suppression (2 nd Harmonic):		-15		dBc
Pushing:		2.0	4.0	MHz/V
Pulling, all Phases:		1.0	2.0	MHz pk-pk
Tuning Sensitivity:		28		MHz/V
Phase Noise @ 10kHz offset:		-103		dBc/Hz
Phase Noise @ 100kHz offset:		-123		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			100	pF
Operating Temperature Range:	-40		+85	$^{\circ}$ C
Storage Temperature Range:	-45		+90	$^{\circ}$ C

Phase Noise (1 Hz BW, Typical)

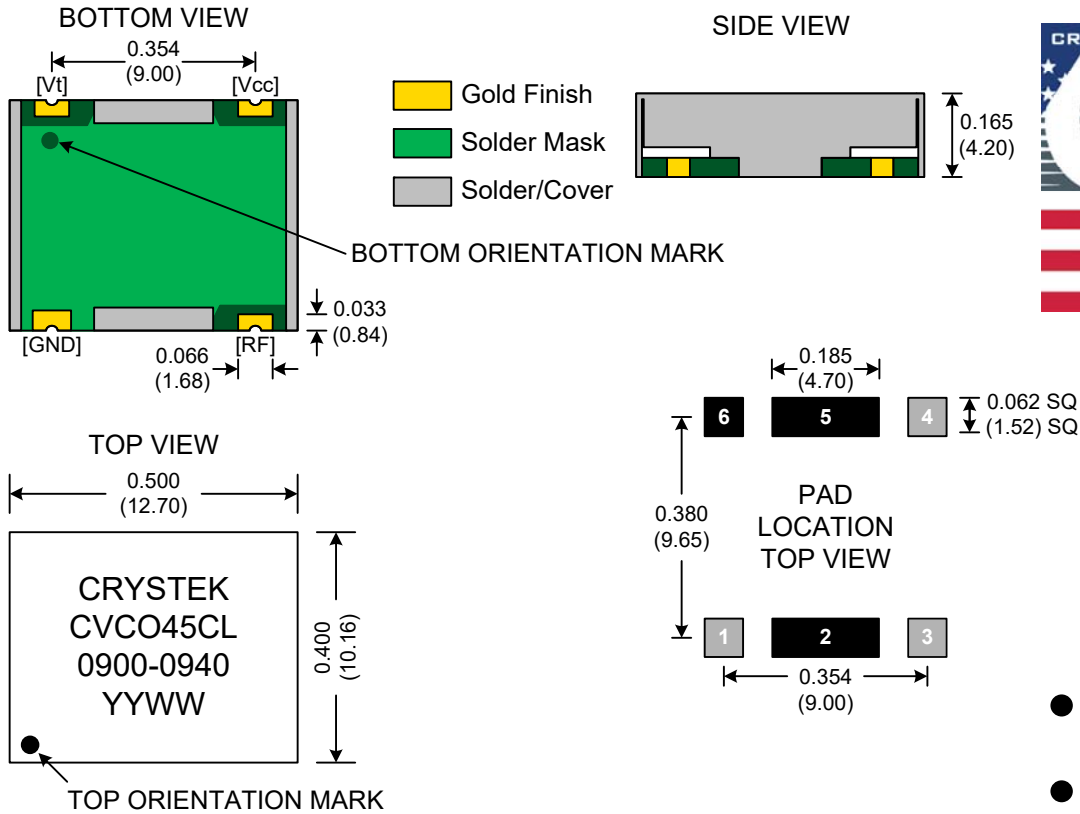


Tuning Curve (Typical)



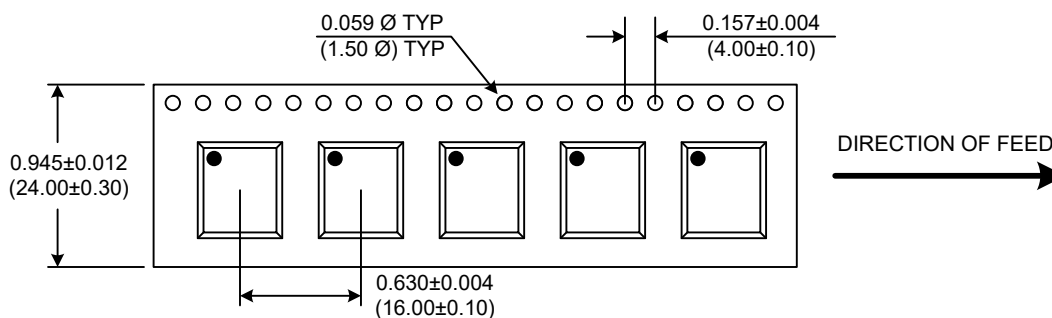
Product Control:

Crystek Part Number:	CVCO45CL-0900-0940	Release Date:	16-Mar-2017
Revision Level:	J	Responsible:	C. Vales



- Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$
- Pad Location Dimensions are in: Inches

TAPE AND REEL



Drawing not to scale

Product Control:

Crystek Part Number:	CVCO45CL-0900-0940	Release Date:	16-Mar-2017
Revision Level:	J	Responsible:	C. Vales

Specification is subject to change without notice